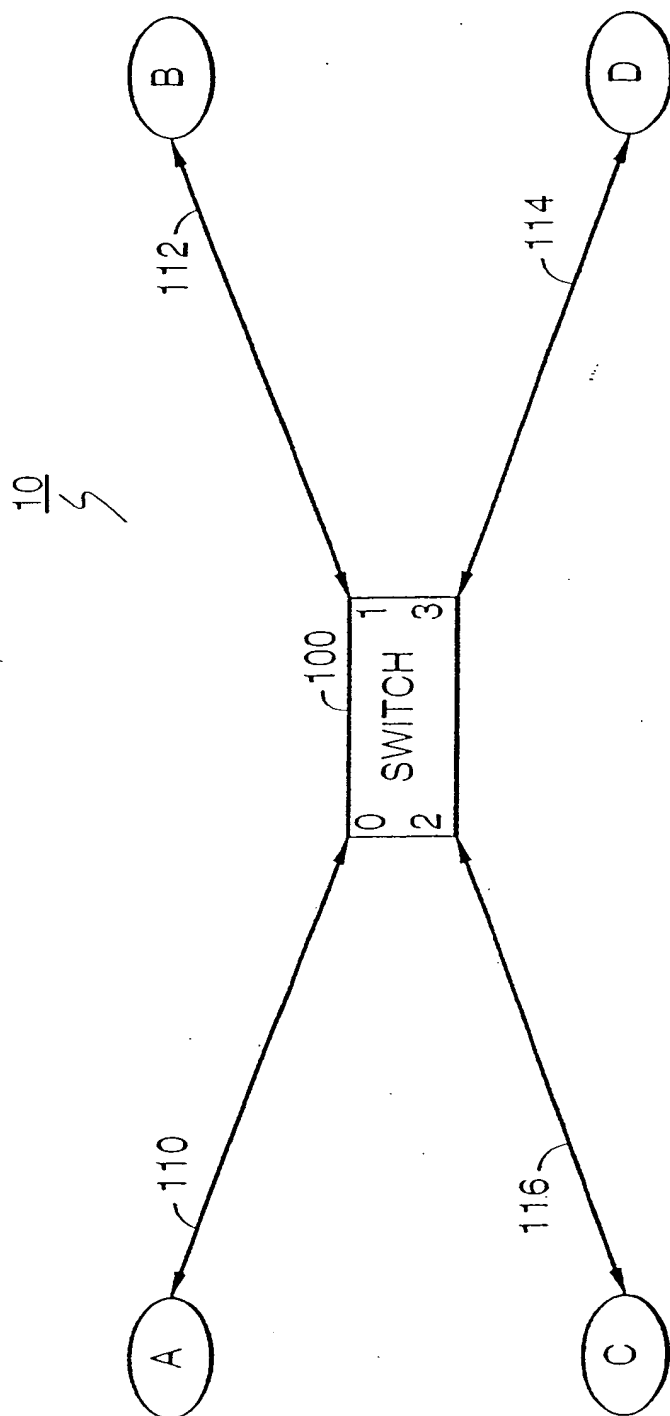


000000-2630960

FIG. 1



**FIG. 2**

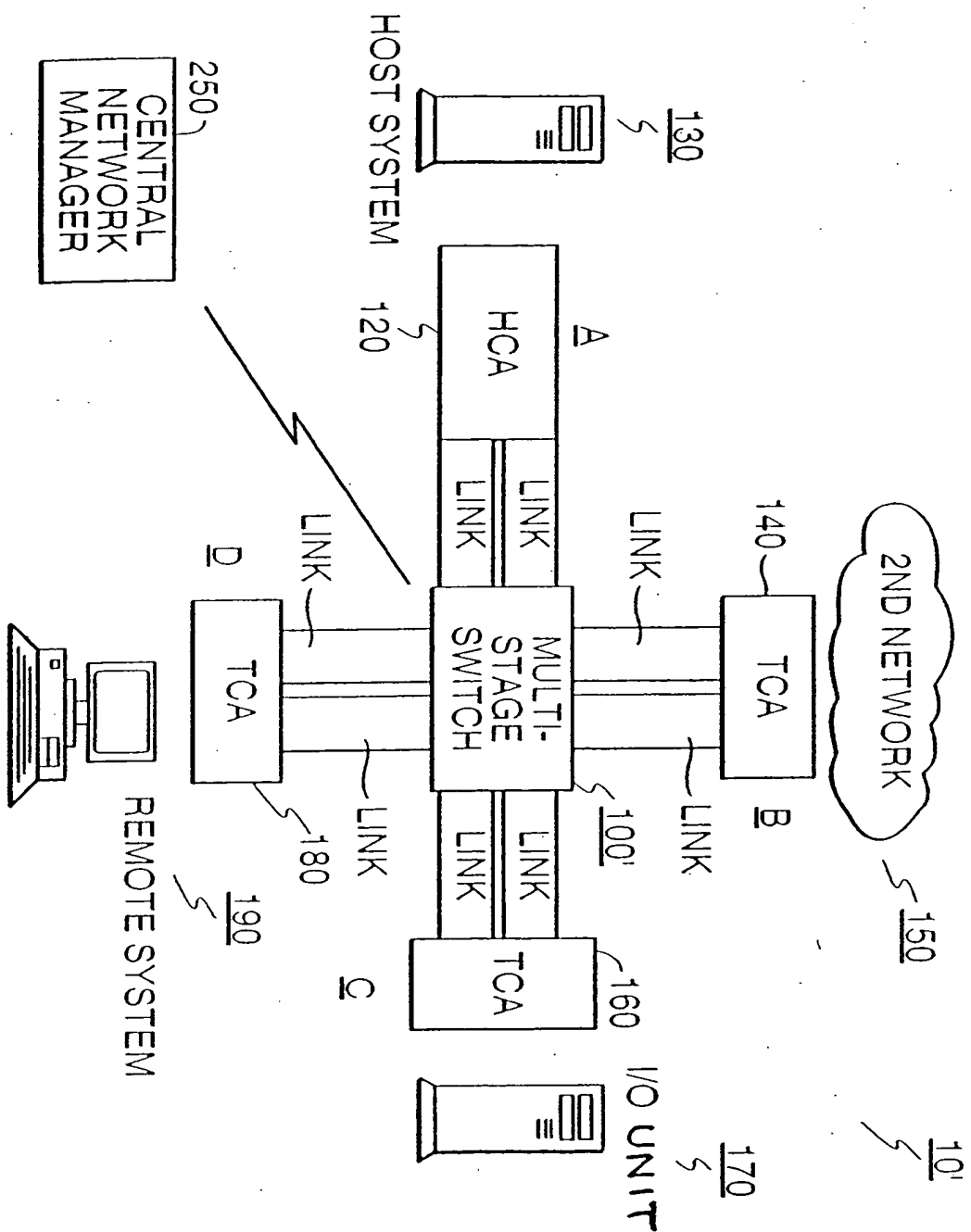


FIG. 3

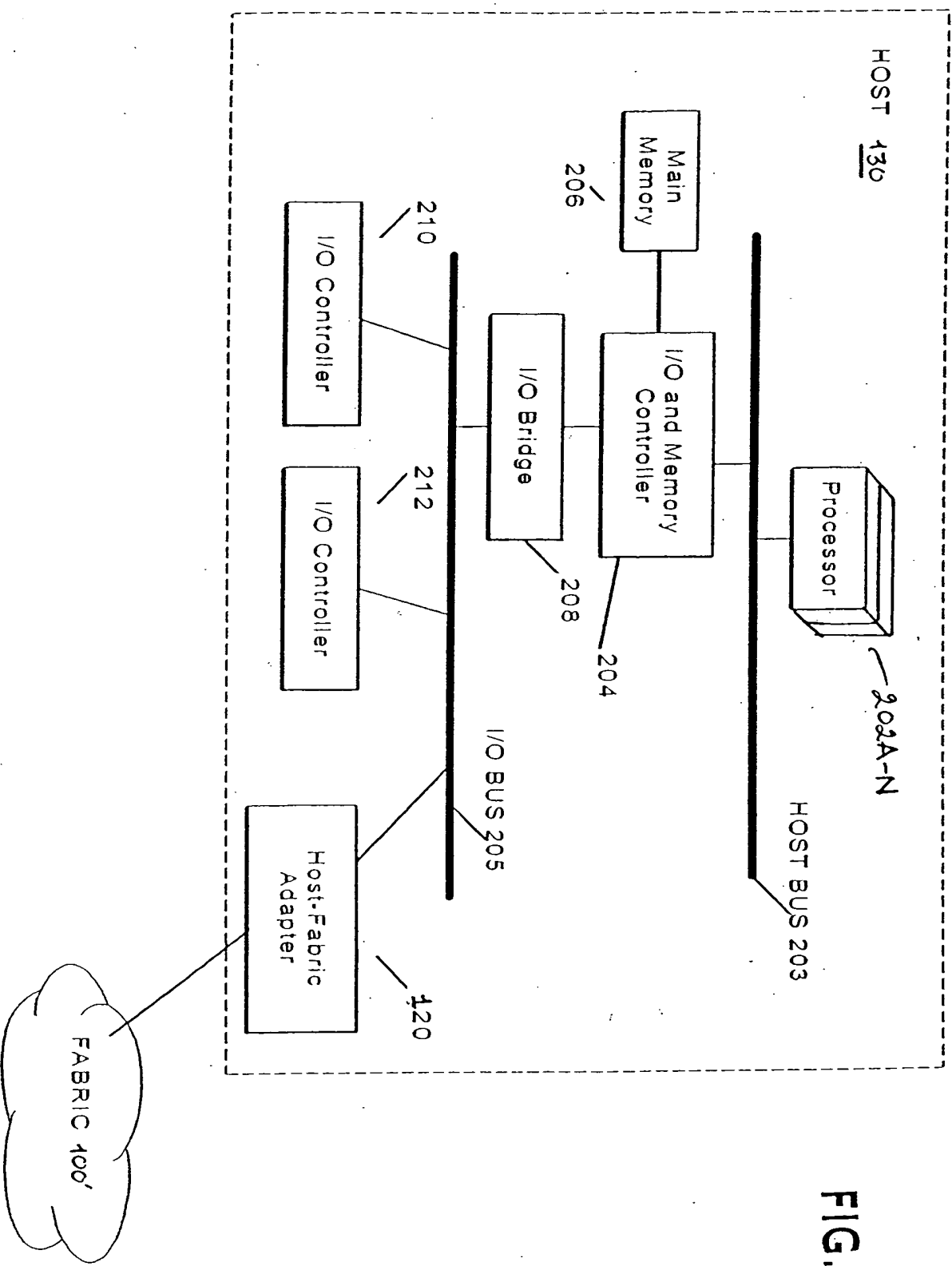
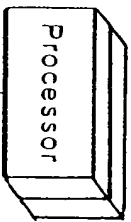


FIG. 3 is a block diagram of a host system 130, which is connected to a fabric 100'.

HOST 130



HOST BUS 203

206

Main  
Memory

I/O and Memory  
Controller

204

Host-Fabric  
Adapter

120

I/O Bridge

208

I/O BUS 205

210

I/O Controller

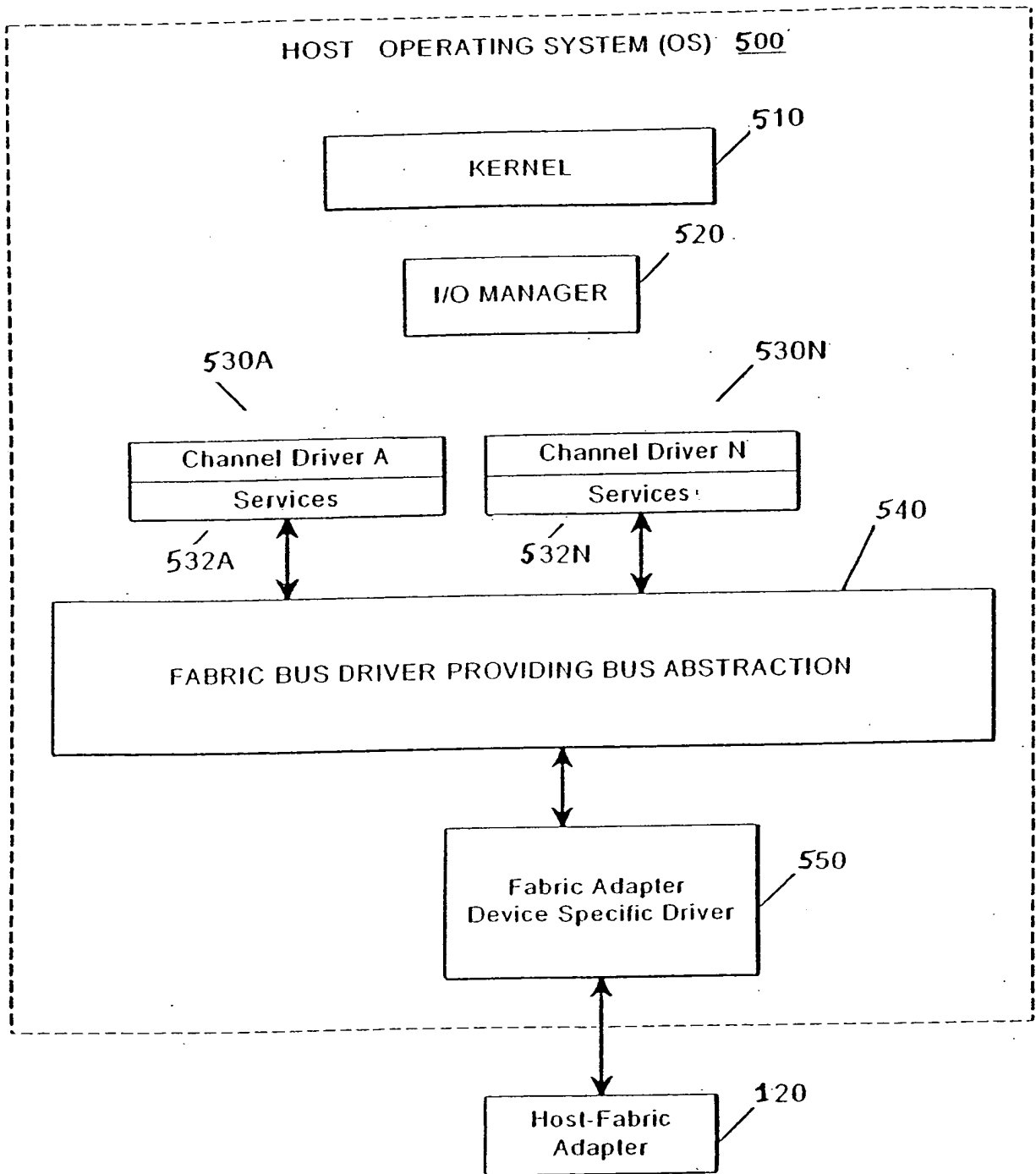
I/O Controller

212

FIG. 4

FABRIC 100'

FIG. 4 is a block diagram of a host system 130 connected to a fabric 100'. The host system 130 includes a processor 202A-N connected to a host bus 203. The host bus 203 is connected to main memory 206 and an I/O and memory controller 204. The I/O and memory controller 204 is connected to a host-fabric adapter 120. The host-fabric adapter 120 is connected to an I/O bridge 208. The I/O bridge 208 is connected to an I/O bus 205. The I/O bus 205 is connected to two I/O controllers 210 and 212. The I/O controllers 210 and 212 are connected to the fabric 100'.

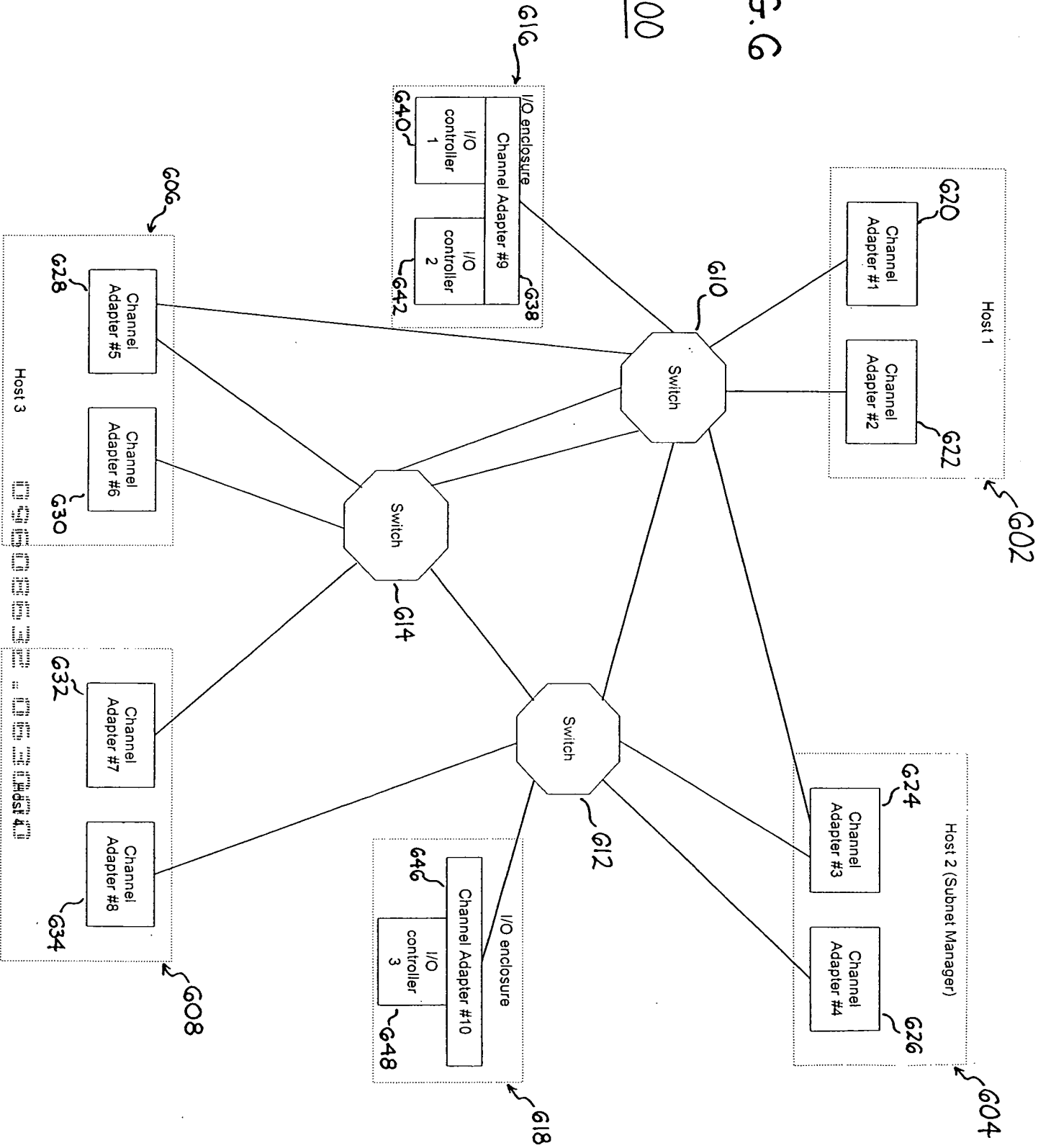


EXAMPLE SOFTWARE DRIVER STACKS FOR HOST SYSTEM

FIG. 5

FIG. 6

600



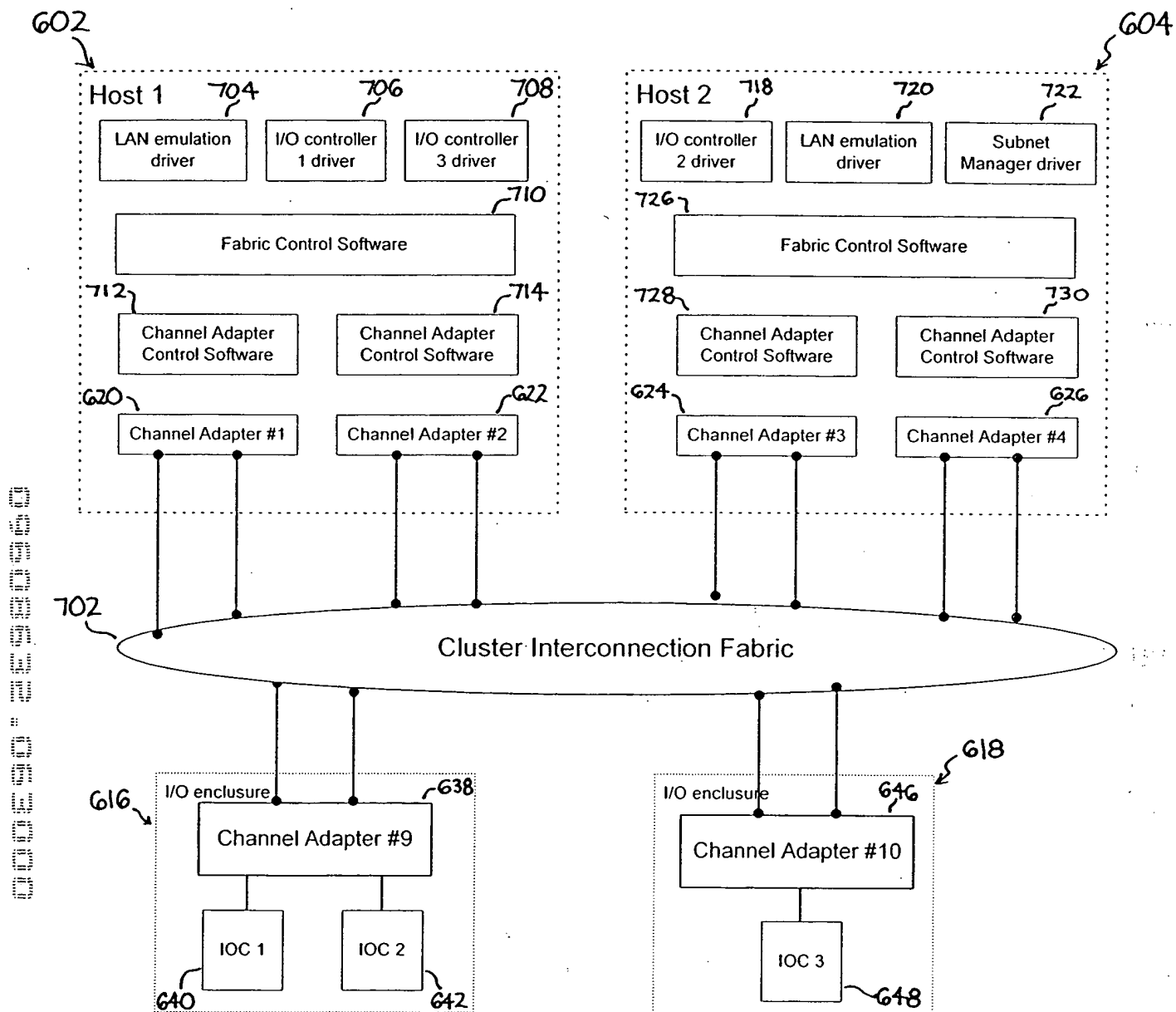
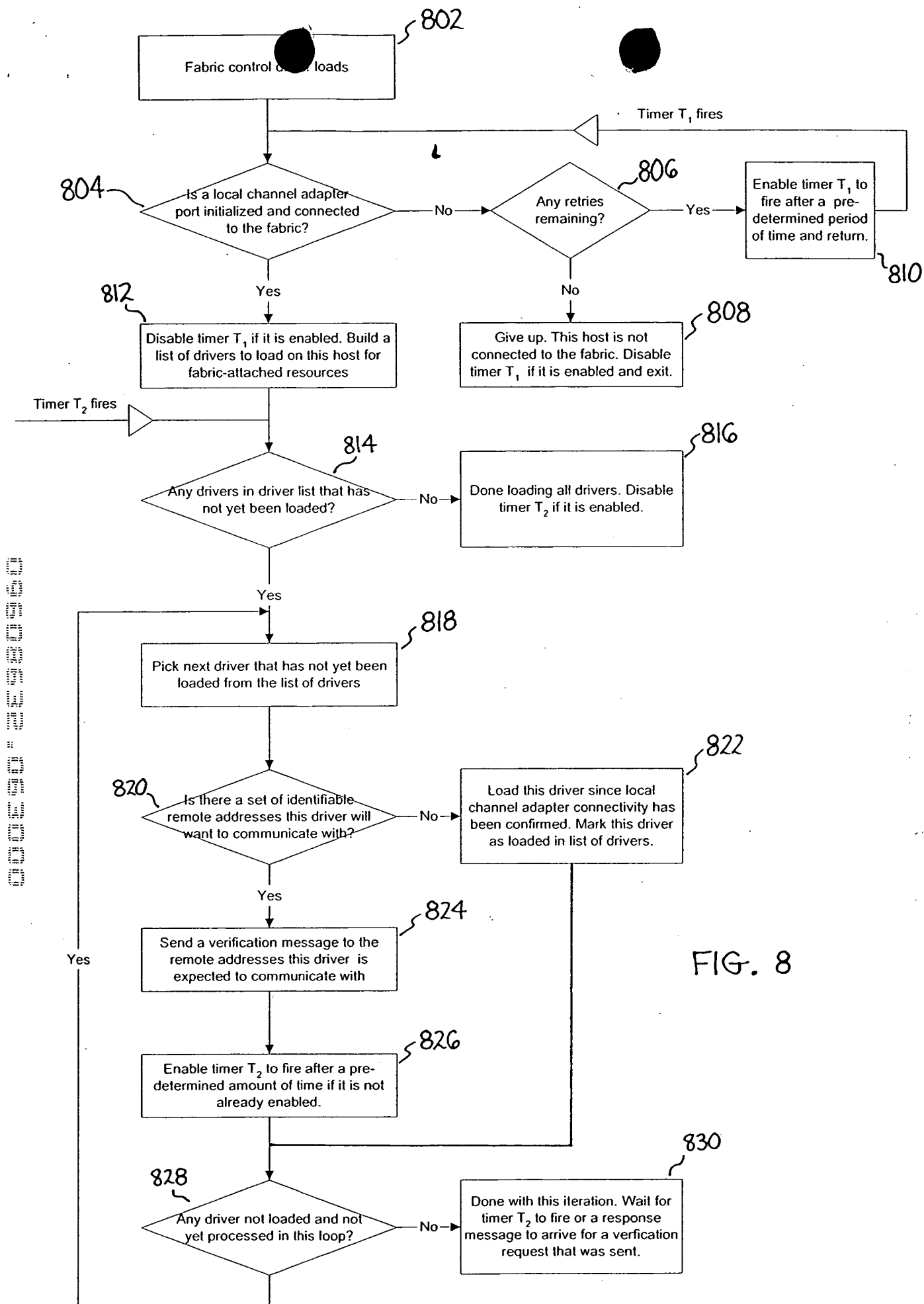


FIG. 7





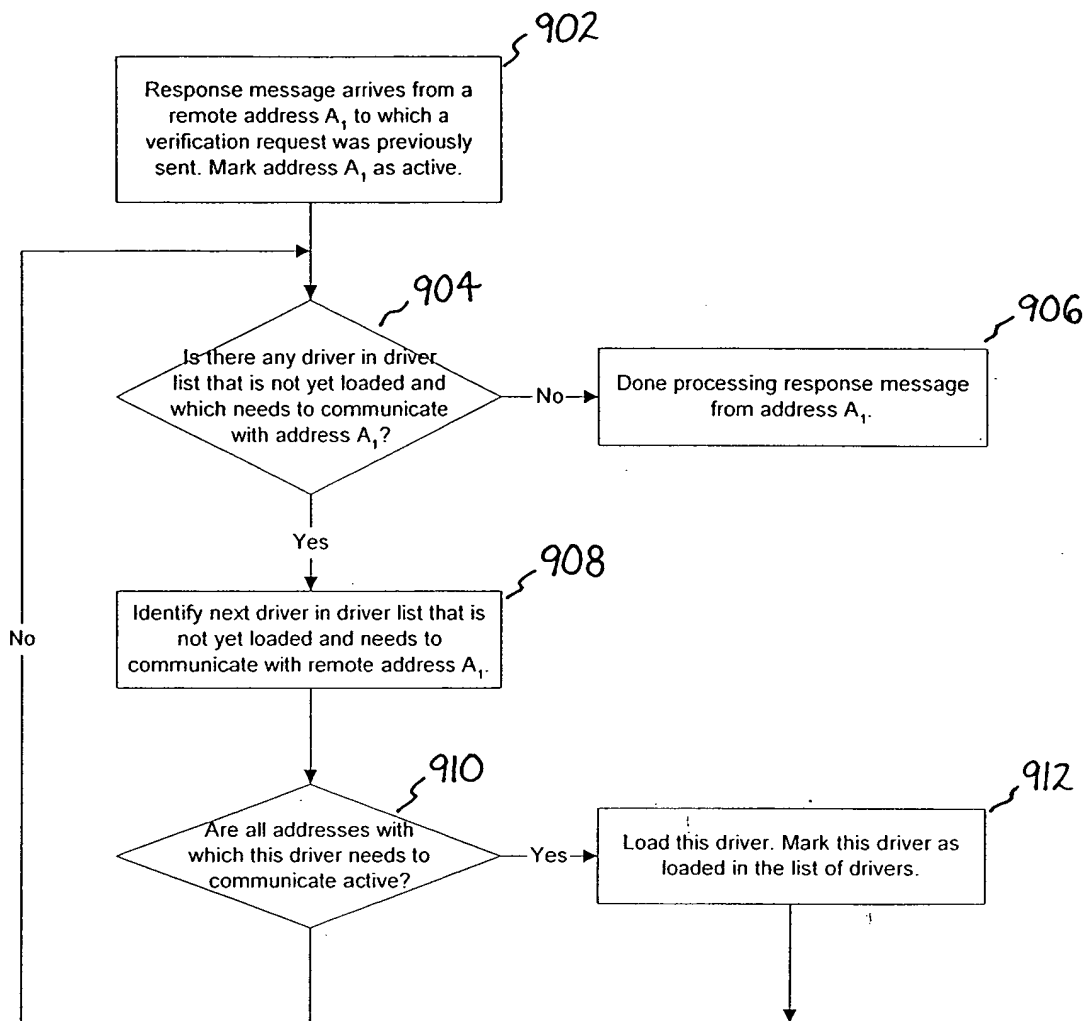


FIG. 9